

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims**

1-30. (Canceled)

1           31. (Previously presented) A program storage device readable by a computer, the  
2 program storage device tangibly embodying one or more programs of instructions executable by  
3 the computer to perform a method for providing a two-step communication scheme, the method  
4 comprising:

5           establishing a portion of memory configured to provide asynchronous, connectionless  
6 inter-process communication between a first process and a second processes;

7           granting exclusive read and write access to a first process to the portion of memory  
8 configured to provide asynchronous, connectionless inter-process communication between the  
9 first process and the second process;

10          while having been granted to the exclusive read and write access to the portion of  
11 memory configured to provide asynchronous, connectionless inter-process communication,  
12 accessing independently of any connection to said second process the portion of memory  
13 configured to provide asynchronous, connectionless inter-process communication by the first  
14 process to modify the contents thereof to provide a message for processing by the second  
15 process; and

16          releasing exclusive read and write access by the first process to the portion of memory  
17 configured to provide asynchronous, connectionless inter-process communication to prevent  
18 inter-process communication between the first and second process from becoming a performance  
19 bottleneck by releasing resources of the first process after the first process modifies the contents  
20 of the portion of memory.

1           32. (Previously presented) The program storage device of claim 31 wherein the method  
2 further comprises configuring the memory to provide header having an operation code and a  
3 parameter region interpreted according to the operation code.

1           33. (Previously Presented) The program storage device of claim 31, wherein the  
2 providing the message into the portion of memory by the first process further comprises  
3 initiating a remote procedure call.

1           34. (Previously presented) The program storage device of claim 31 wherein the method  
2 further comprises granting exclusive read and write access to the second process to the portion of  
3 memory configured to provide asynchronous, connectionless inter-process communication, while  
4 having been granted to the exclusive read and write access to the portion of memory, accessing  
5 independently of any connection to said first process the portion of memory by the second  
6 process to access the message provided in the portion of memory by the first process and  
7 releasing exclusive read and write access by the second process to the portion of memory.

1           35. (Previously presented) The program storage device of claim 34 wherein the method  
2 further comprises:

3           establishing exclusive read and write access to the portion of memory by the second  
4 process;

5           accessing independently of any connection to said first process the portion of memory by  
6 the second process to provide a result message in response to the message placed in the portion  
7 of memory by the first process;

8           releasing exclusive read and write access by the second process to the portion of memory;

9           and

10          providing by the second process a notification to the first process to check the portion of  
11 memory.

1           36. (Previously presented) The program storage device of claim 31 wherein the method  
2 further comprises providing by the first process a notification to the second process to check the  
3 portion of memory.

1           37. (Previously presented) A server comprising a memory, wherein a portion of the  
2 memory is configured to provide two-step, asynchronous, connectionless inter-process  
3 communication between a first process and a second process, the portion of memory being  
4 configured as memory accessible by the first and second processes by selective granting, wherein  
5 read and write access to the portion of memory being granted exclusively to the first process for  
6 modification of contents of the portion of memory independently of any connection to said  
7 second process to prevent inter-process communication between the first and second process  
8 from becoming a performance bottleneck by releasing resources of the first process after the first  
9 process modifies the contents of the portion of memory.

1           38. (Previously Presented) The server of claim 37, wherein the portion of memory  
2 comprises a slot having a header comprising an operation code and a parameter region  
3 interpreted according to the operation code.

1           39. (Previously Presented) The server of claim 37, wherein the placing message into the  
2 portion of memory by the first process further comprises initiating a remote procedure call.

1           40. (previously presented) The server of claim 37, wherein the first process releases  
2 exclusive read and write access by the first process to the portion of memory, the second process  
3 is granted exclusive read and write access to the portion of memory configured to provide  
4 asynchronous, connectionless inter-process communication, accesses the portion of memory  
5 independently of any connection to said first process to access a message provided in the  
6 portion of memory by the first process and releases exclusive read and write access by the  
7 second process to the portion of memory.

1           41. (Previously presented) The server of claim 40, wherein the second process is granted  
2 exclusive read and write access to the portion of memory, accesses the portion of memory  
3 independently of any connection to said first process to provide a result message in response to  
4 the message placed in the portion of memory by the first process, releases exclusive read and  
5 write access by the second process to the portion of memory and provides a notification to the  
6 first process to check the portion of memory.

1           42. (Previously Presented) The server of claim 37, wherein the first process provides a  
2 notification to the second process to check the portion of memory.

1           43. (Previously presented) A system, comprising:  
2 a first process;  
3 a second process; and  
4 memory configured to provide asynchronous, interprocess communication between the  
5 first process and the second process, wherein the memory provides a portion of memory  
6 configured to be accessible by the first and second processes by selective granting, wherein read  
7 and write access to the portion of memory is granted exclusively to the first process for  
8 modification of contents of the portion of memory to prevent inter-process communication  
9 between the first and second process from becoming a performance bottleneck by releasing  
10 resources of the first process after the first process modifies the contents of the portion of  
11 memory.

1           44. (Previously Presented) The system of claim 43, wherein the portion of memory  
2 comprises a slot having a header comprising an operation code and a parameter region  
3 interpreted according to the operation code.

1           45. (Previously Presented) The system of claim 43, wherein the placing message into the  
2 portion of memory by the first process further comprises initiating a remote procedure call.

1           46. (previously presented) The system of claim 43, wherein the first process releases  
2 exclusive read and write access by the first process to the portion of memory, the second process  
3 is granted exclusive read and write access to the portion of memory configured to provide  
4 asynchronous, connectionless inter-process communication, accesses the portion of memory to  
5 access a message provided in the portion of memory by the first process and releases exclusive  
6 read and write access by the second process to the portion of memory.

1           47. (Previously presented) The system of claim 46, wherein the second process is  
2 granted exclusive read and write access to the portion of memory, accesses the portion of  
3 memory to provide a result message in response to the message placed in the portion of memory  
4 by the first process, releases exclusive read and write access by the second process to the portion  
5 of memory and provides a notification to the first process to check the portion of memory.

1           48. (Previously Presented) The system of claim 43, wherein the first process provides a  
2 notification to the second process to check the portion of memory.

1           49. (Previously presented) A service level agreement (SLA) server, comprising:  
2 a plurality of processes, the plurality of processes comprising a database manager for  
3 managing performance data, an application server for collecting performance data and providing  
4 a client interface for establishing service level agreements, a SLA core for analyzing data and  
5 controlling actions based on service level agreements and policy and a performance monitor  
6 daemon for communicating with remote I/O service gateways to collect data and send throttling  
7 requests; and

8           memory configured to provide asynchronous, interprocess communication between the  
9 processes, wherein the memory provides a portion of memory configured to be accessible by the  
10 processes by selective granting, wherein read and write access to the portion of memory is  
11 granted exclusively to a first of the processes for modification of contents of the portion of  
12 memory to prevent inter-process communication between the process from becoming a  
13 performance bottleneck by releasing resources of the first of the processes after the first of the  
14 processes modifies the contents of the portion of memory.

1           50. (Previously presented) A service level agreement (SLA) server, comprising:  
2           a processor configured for providing a plurality of processes; and  
3           memory configured to provide asynchronous, interprocess communication between the  
4 first process and the second process, wherein the memory provides a portion of memory  
5 configured to be accessible by the first and second processes by selective granting, wherein the  
6 processor grants exclusive read and write access to the portion of memory by the first process for  
7 modification of contents of the portion of memory to prevent interprocess communication  
8 between the first and second process from becoming a performance bottleneck by releasing  
9 resources of the first process after the first process modifies the contents of the portion of  
10 memory.

1           51. (Previously presented) A method for providing a two-step communication scheme,  
2 comprising:  
3           establishing a portion of memory configured to provide asynchronous, connectionless  
4 inter-process communication between a first process and a second processes;  
5           granting exclusive read and write access to a first process to the portion of memory  
6 configured to provide asynchronous, connectionless inter-process communication between the  
7 first process and the second process;  
8           while having been granted to the exclusive read and write access to the portion of  
9 memory configured to provide asynchronous, connectionless inter-process communication,  
10 accessing independently of any connection to said second process the portion of memory  
11 configured to provide asynchronous, connectionless inter-process communication by the first  
12 process to modify the contents thereof to provide a message for processing by the second  
13 process; and  
14           releasing exclusive read and write access by the first process to the portion of memory  
15 configured to provide asynchronous, connectionless inter-process communication to prevent  
16 inter-process communication between the first and second process from becoming a performance  
17 bottleneck by releasing resources of the first process after the first process modifies the contents  
18 of the portion of memory.

1           52. (Previously presented) A server comprising a means for storing data, wherein a  
2     portion of the means for storing data is configured to provide two-step, asynchronous,  
3     connectionless inter-process communication between a first process and a second process, the  
4     portion of the means for storing data is configured to be accessible by the first and second  
5     processes by selective granting, wherein read and write access to the portion of the means for  
6     storing data is granted exclusively to the first process for modification of contents of the portion  
7     of the means for storing data independently of any connection to said second process to prevent  
8     inter-process communication between the first and second process from becoming a performance  
9     bottleneck by releasing resources of the first process after the first process modifies the contents  
10    of the portion of the means for storing data.

1           53. (Previously presented) A system, comprising:  
2     first process means;  
3     second process means;  
4     means for storing data configured to provide asynchronous, interprocess communication  
5     between the first process means and the second process means, wherein the means for storing  
6     data is configured to be accessible by the first and second process means by selective granting,  
7     wherein read and write access to the portion of the means for storing data is granted exclusively  
8     to the first process means for modification of contents of the portion of the means for storing  
9     data to prevent inter-process communication between the first and second process means from  
10    becoming a performance bottleneck by releasing resources of the first process means after the  
11    first process means modifies the contents of the portion of the means for storing data.

1           54. (Previously presented) A service level agreement (SLA) server, comprising:  
2           a plurality of process means, the plurality of processes comprising process means for  
3 managing performance data, process means for collecting performance data and providing a  
4 client interface for establishing service level agreements, process means for analyzing data and  
5 controlling actions based on service level agreements and policy and process means for  
6 communicating with remote I/O service gateways to collect data and send throttling requests;  
7 and  
8           means for storing data configured to provide asynchronous, interprocess communication  
9 between the plurality of process means, wherein the means for storing data is accessible by the  
10 plurality of process means by selective granting, wherein read and write access to the means for  
11 storing data is granted exclusively to a first of the process means for modification of contents of  
12 the means for storing data to prevent inter-process communication between the plurality of  
13 process means from becoming a performance bottleneck by releasing resources of the first of the  
14 process means after the first of the process means modifies the contents of the portion of means  
15 for storing data.